

Fig.1 Geometry of antenna

The geometry of the proposed antenna structure is shown in Fig.1. The Microstrip antenna is fabricated on upper side of substrate FR4 and RIS is placed on bottom side of substrate FR4. The FR4 substrate is at 1 mm from ground. The parasitic patches are fabricated on the lower side of FR4 superstrate which is of thickness 1.59 mm and located at height of  $h_s$  from ground plane. Relative permittivity and loss tangent of this superstrate is 4.4 and 0.02 respectively. In order to achieve high efficiency, air gap acts as a dielectric medium between superstrate and feed patch. A coaxial probe of 50Ω is used to feed MSA. The diagonal feed and two shorting pins along X axis on both side of the centre of MSA are used to achieve circularly polarized waves. The antenna is designed to operate over 5.725– 5.875 GHz ISM band. All dimensions mentioned here are in mm.

### 3. Analysis on infinite ground plane

Microstrip antenna is fabricated on upper side of substrate of thickness 1.59 mm and RIS is placed on bottom side of substrate. This substrate is at 1 mm from ground plane which is infinite and perfectly reflecting. The length and width of MSA is 14.3 mm and 14.7 mm respectively. MSA is fed diagonally at (3, 3.1). Circular polarization is achieved by using two shorting pins. This method also suppresses unwanted modes and helps to improve quality

of circular polarization achieved. RIS consists of square patches of 3 mm with spacing of 1 mm. Array of parasitic patches are fabricated on lower side of superstrate layer of thickness 1.59 mm and it is placed at height of  $\lambda_0/2$  from feed patch. The MSA dimension, MSA height, parasitic patch dimension, spacing between parasitic patches, shorting pin locations are optimized in order to improve gain of an antenna.

Variation of gain of MSA0 ( superstrate with no parasitic patch), 1×1, 2×2, 3×3, 4×4 and 5×5 structures is shown Fig 2(a) and 2(b). Variation of axial ratio and VSWR of 3×3, 4×4 and 5×5 is shown in Fig 3 and 4 respectively.

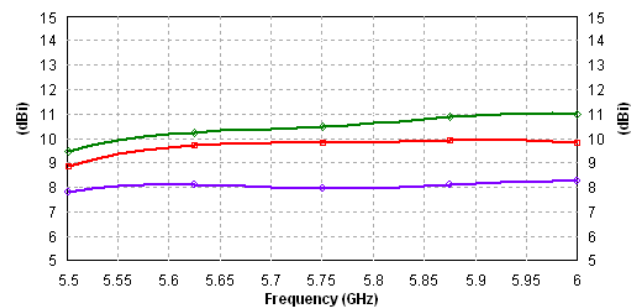


Fig.2 (a) Gain variations vs. frequency ( MSA0, MSA 1x1, MSA 2x2)

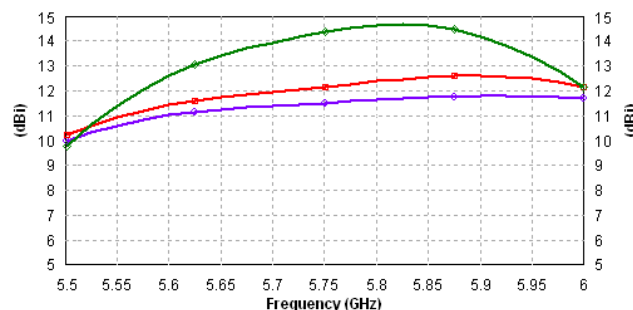


Fig.2 (b) Gain variations vs. frequency ( MSA 3x3, MSA 4x4, MSA 5x5)

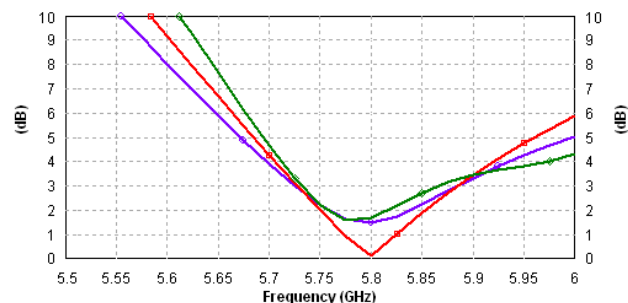


Fig.3 Axial ratio vs. frequency ( MSA 3x3, MSA 4x4, MSA 5x5)

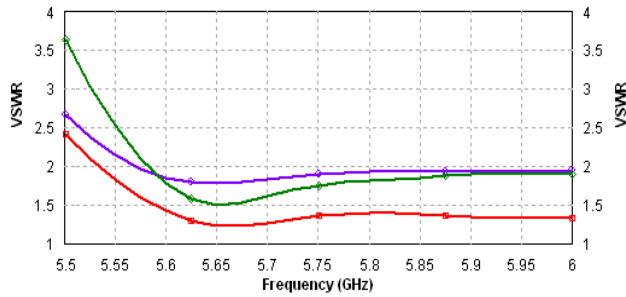


Fig.4 VSWR ratio vs. frequency ( — MSA 3x3  
 — MSA 4x4, — MSA 5x5)

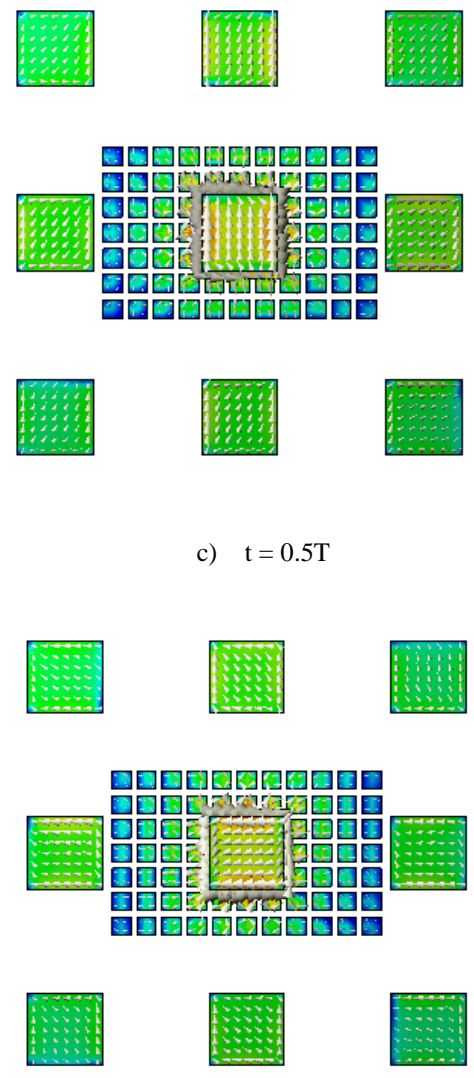
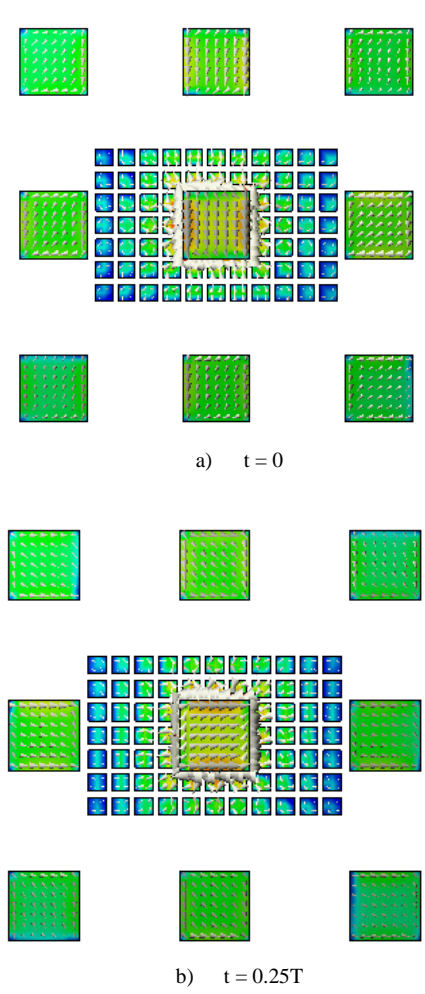


Fig.5. Current distribution of 3x3 array structure at 5.8 GHz

Average and vector current distribution at the feed, parasitic patches and RIS at 5.8 GHz at different time that is at  $t = 0$ ,  $t = 0.25T$ ,  $t = 0.5T$ ,  $t = 0.75T$  is shown in fig.5. Here  $T$  is time period of the wave at central operating frequency 5.8 GHz. From current distribution, it is clear that the fields rotate clockwise by  $90^\circ$  in order to produce right handed circular polarization. The amplitude of current induced in parasitic patches is nearly in phase and decrease as its distance from feed element increases.

#### 4. Conclusion

High gain right hand circularly polarized microstrip antenna is proposed here. The feed patch is placed in fabry perot cavity to increase the gain. The gain is further

improved by using RIS layer which also reduces size. Gain increases as we increase the size of array of parasitic patches on superstrate layer. The proposed antenna can be used for satellite and terrestrial communication.

## 5. References

- [1] Steven (Shichang) Gao, Qi Luo, Fuguo Zhu, Circularly Polarized Antenna, John Wiley and sons, 2013.
- [2] V. G. Kasabegoudar and K. J. Vinoy, "A broadband suspended microstrip antenna for circular polarization", *Progress in Electromagnetic Research*, vol. 90, 1990, pp. 353-368.
- [3] G. Kumar and K. P. Ray, *Broadband Microstrip Antennas*, Norwood, MA Artech house, 2003.
- [4] Chih-Yu Huang, Jian-Yi Wu and Kin-Lu Wong, "High-gain compact circularly polarised microstrip antenna", in *IEEE*, 1998.
- [5] Nasimuddin, Zhi Ning Chen, Xianming Qing, "A Compact Circularly Polarized Slotted-Slit-Microstrip Patch Antenna", *Proceedings of the Asia-Pacific Microwave Conference*, 2011.
- [6] Jianjun Wu, Xueshi Ren, Zhaoxing Li, and Yingzeng Yin. "Modified square slot antennas for broad band Circular Polarization", *Progress In Electromagnetics Research C*, Vol. 38, 2013, pp. 1-14.
- [7] K. L. Wong and J. Y. Wu, "Bandwidth Enhancement of Circularly Polarized Microstrip Antenna Using Chip Resistor Loading", in *Electronics Letters*, Vol. 33, No. 21, 1997, pp. 1749–1751.
- [8] Ferrero F., Luxey C., Jacquemod G., Staraj R., "Dual-band circularly polarized microstrip antenna for satellite applications," in *IEEE Antennas and Wireless Propagation Letters*, vol.4, 2005, pp. 13-15.
- [9] K. Agarwal, Yong Xin Guo, Nasimuddin, A. Alphones, "Dual-band circularly polarized stacked microstrip antenna over RIS for GPS applications", in *Wireless Symposium (IWS)*, 2013 *IEEE International*, April 2013, pp-14-18.
- [10] Kamal Sarabandi, Amelia M. Buerkle, and Hossein Mosallaei, "Compact Wideband UHF Patch Antenna on a Reactive Impedance Surface", in *IEEE antennas and wireless propagation letters*, vol. 5, 2006, pp-503-506.
- [11] P.N.Chine and Girish Kumar, "Three Dimensional, Efficient, Directive Microstrip Antenna Arrays," *IEEE Int. Symposium Antenna and Propagation*. Washington DC., July 2005.
- [12] R. K. Gupta and J. Mukherjee, "Low cost efficient high gain antenna using array of parasitic patches on a superstrate layer", *Microw. Opt. Technol. Lett.* 51, 2009, pp. 733– 739.
- [13] H. Mosallaei and K. Sarabandi, "Antenna miniaturization and bandwidth enhancement using a reactive impedance substrate," *IEEE Trans. Antennas Propag.*, vol. 52, no. 9, Sep 2004, pp. 2403–2414.



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